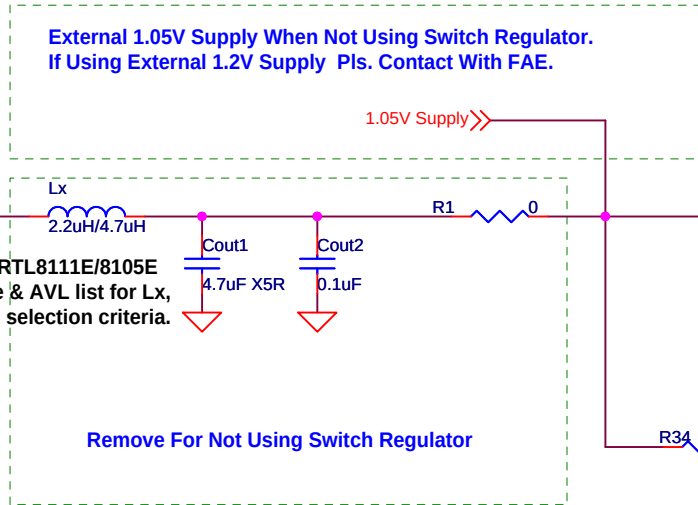


RTL8111E/RTL8105E 48Pin Reference Schematic V106

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1. Title page
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3. CONTROLLER
4. Optional Function
5. Change Notes

Realtek Semiconductor Corp.		
No.2, Industry E. Rd. IX, Science-Based Industrial Park, Hsinchu, Taiwan 300		
Title RTL81111E/8105E Reference circuit: Index		
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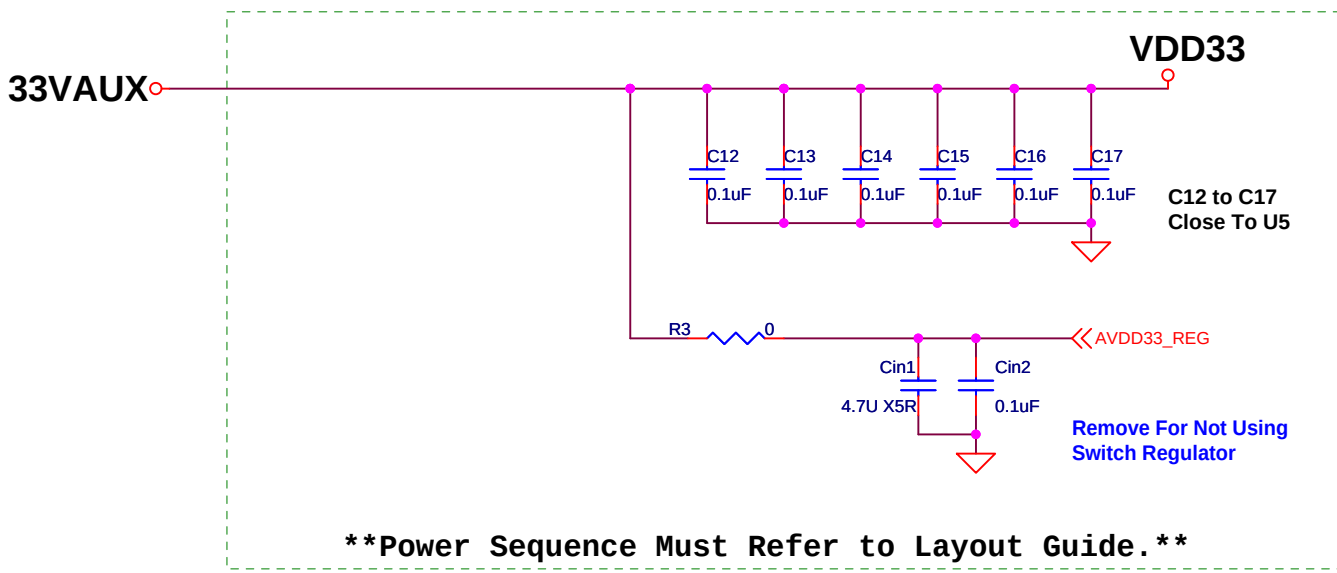


For RTL8111E-VB
* C1 to C7 are for VDD10 pins-- 3, 6, 9, 13, 29, 41, 45.

For RTL8105E-VB
* C1 to C4 are for VDD10 pins-- 3, 13, 29, 45.

For RTL8105E-VC
* C1 to C3 are for VDD10 pins-- 13, 29, 45.

	mode	Lx	Cout1,2	R1	Cin1,2	R3	R23	R24
8111E-VB/8105E-VB/8105E-VC	SWR	O	O	O	O	O	O	X
8105E-VC	LDO	X	X	X	X	X	X	O
8111E-VB/8105E-VB	External	X	X	X	X	X	X	O
8105E-VC	External	X	X	X	X	X	O	X

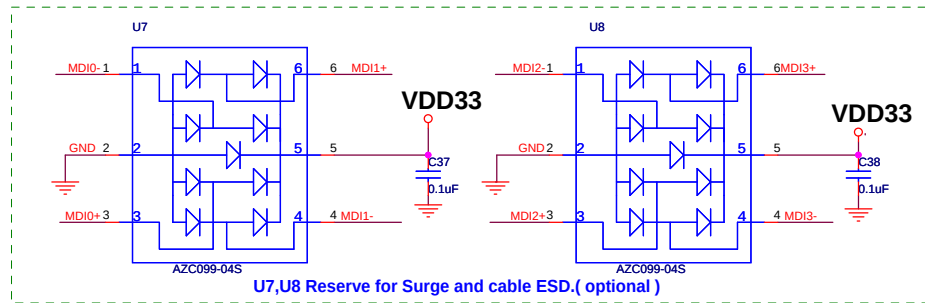
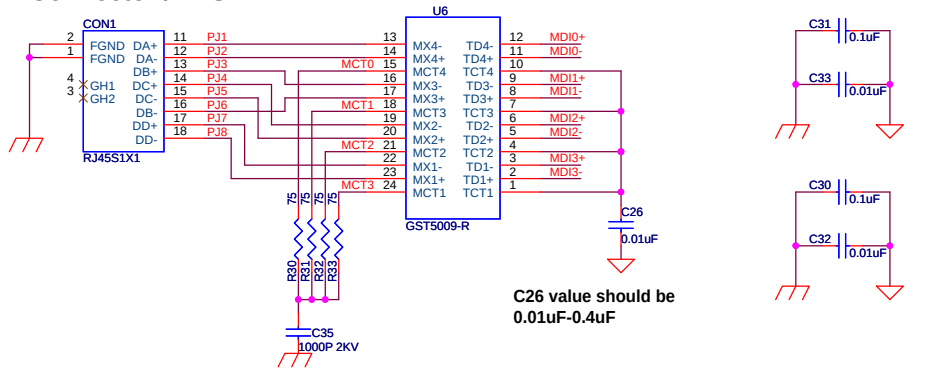


For RTL8111E-VB
* C12 to C17 are for VDD33 pins-- 12, 27, 39, 42, 47, 48.

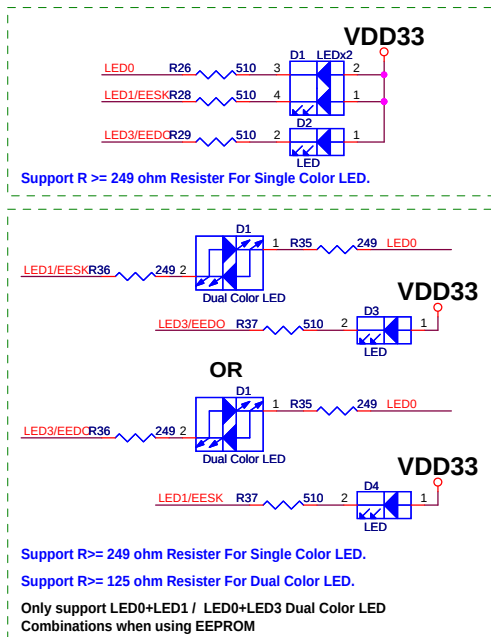
For RTL8105E-VB
* C12 to C16 are for VDD33 pins-- 27, 39, 42, 47, 48.

For RTL8105E-VC
* C12 to C15 are for VDD33 pins-- 27, 39, 47, 48.

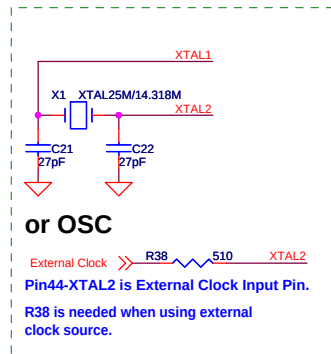
Connector / TVS



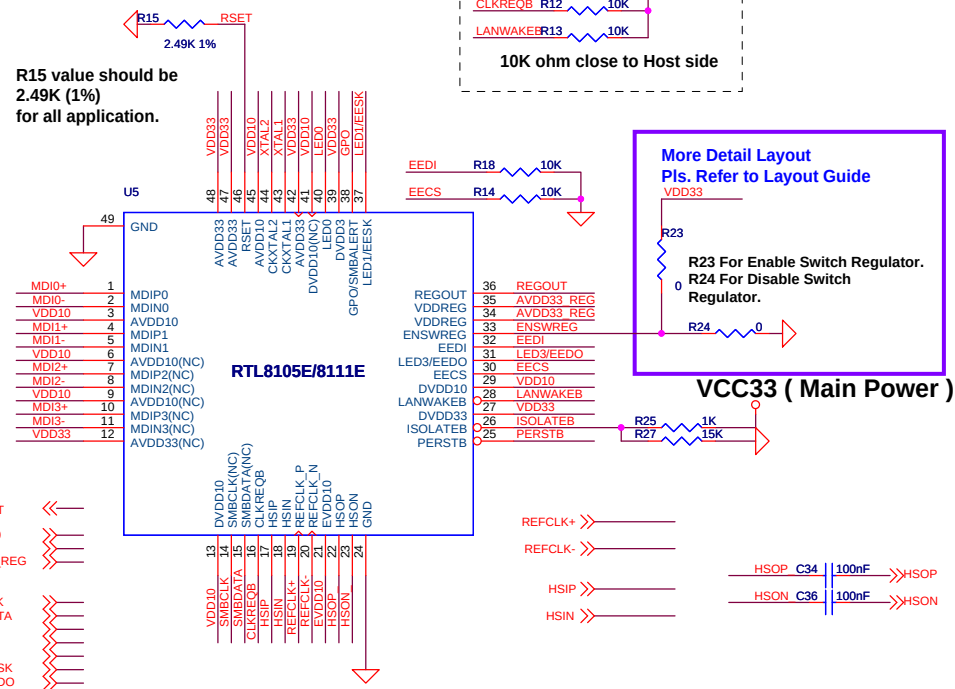
LED



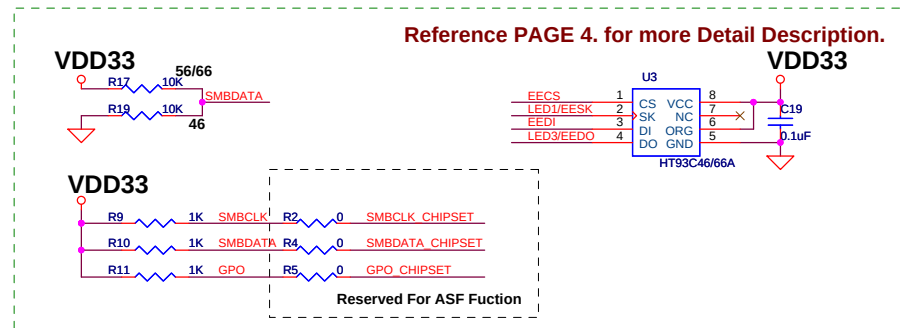
XTAL



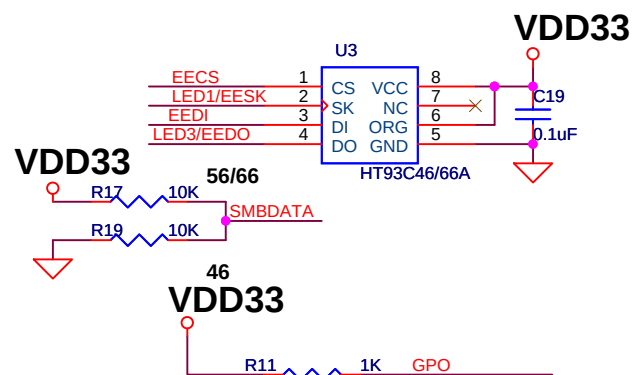
Controller



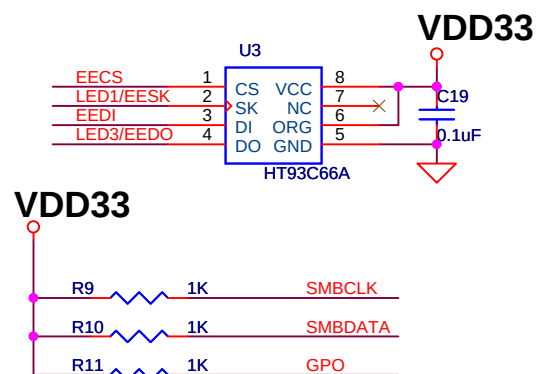
Function (EEPROM,Efues,ASF)



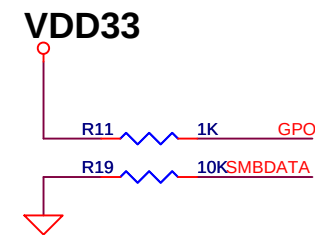
For RTL8111E-VB EEPROM / ASF / Efuse Function (3 Option)



1.When using EEPROM only without ASF function.
Using R17 For 93C56/66
Using R19 For 93C46

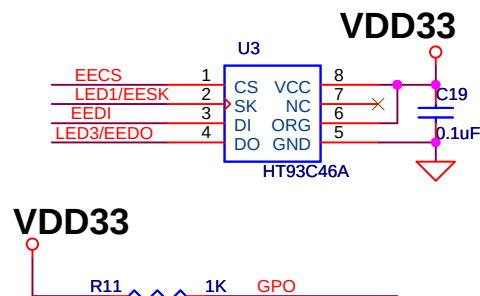


2.When Using EEPROM(Only 93C66) With ASF Function

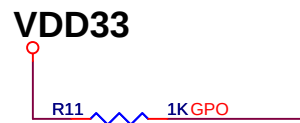


3.When using EFuse only without ASF function.

For RTL8105E-VB/8105E-VC EEPROM / Efuse Function (2 Option)



1.When using EEPROM(93C46A)



2.When using EFuse.

Change Notice from 1.00 to 1.01

1. C17 rename to Cin1, C18 rename to Cin2. C8 rename to Cout1, C9 rename to Cout2, L1 rename to Lx to align with SWR Layout guide (Page 2.)
2. Add supported resistor values for Dual Color LED, supported combinations when using EEPROM (Page 3.)
3. Add R38 for external clock circuit. (Page 3.)

Change Notice from 1.01 to 1.02

1. Update Pin name(Pin3, Pin47) For RTL8105E.(Page 3.)
2. Update Capacitor Description For RTL8105E and RTL8111E. (Page 2.)
3. Modify Center tap Schematic for Power Optimization. Center tap short and coconnector 1uF to GND.(Page 3.)
4. Add RTL8105 Optional Function Schematic.(Page 4.)

Change Notice from 1.02 to 1.03

1. Modify Efuse Autoload Schematic, SMBData Pull Down. (Page 4.)

Change Notice from 1.03 to 1.04

1. Remove TWSI Schematic. (Page 3 & 4.)
2. Remove TWSI Share Pin Name. (Page 3 & 4.)
3. Add Note For Lanwakeb & Clkreqb.(Page 3.)
4. Add Note For Function Option for Using Efuse/BIOS Patch.(Page 4.)

Change Notice from 1.04 to 1.05

1. Add Resister For SMB Bus.(Page 3.)
2. Add C26 Value Note.(Page 3.)
3. Add SWR Power Sequence Note.(Page2.)

Change Notice from 1.05 to 1.06

1. Modify Power Mode Table. (Page 2.)
2. Remove C4, C16 For RTL8105E-VC. (Page 2.)
3. Modify Lx Value and Update Note. (Page 2.)
4. Modify C26 Value Note. (Page 3.)

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